# **Sensor Platform**

# PSU Capstone Project

# WPR

Last Week: 2/21 – 2/27

Next Week: 2/28 – 3/6

# Colten Nye

## Last week:

* Worked on project documentation:
  + Created formal Requirements Specification.
  + Create Test Plan.
  + Improved Project Schedule.
  + Cleaned up BOM document.
  + Cleaned up UI mockup.
  + Composed readme.txt of Project Documents directory.
* Attended proposal presentation.
  + Presented.
  + Took notes.
  + Coordinated video conferencing with sponsors.
* Started Slack.com team for flexible and effective communication.
* Started checkvist.com list for use as a master task-list.
* Studied MS Project tutorial videos to make most use of the software.
* Started revision of documentation as per Dr. Zurk’s feedback.

## Next week:

* Complete revision of documentation.
* Submit revised documentation to sponsors and supervisor.
* Learn chrome extension programming.
* Learn about In-Application-Programming.
* Finalize UI mockup.

## Issues:

* We are behind schedule.

# Golriz Sedaghat

## Last week:

* Created the powerpoint format of the revised proposal.
* Did research about other comparable systems to our project.
* Did research about implementing SD card and USB device in the design/schematic.
* Created the bill of material.
* Revised the schematic.
* Created the pin list for the schematic.

## Next week:

* Doing research about charging options of the circuit and its implementation in the current design
* Meeting with Mr. Greenburg on Tuesday and checking the current schematic with him, and based on the given feedback, creating the PCB layout

## Issues:

* Don’t know how to use RTC and timer features of stm32F2X in the hardware design
* Creating a detailed TO DO’s list for the project and dividing tasks evenly among three of us.

Steve Peirce

This week:  
-Met with team twice to talk over detail and prepare for proposal  
-Had proposal meeting with Dr. Zurk  
-Ordered and received evaluation board and STLINK JTAG unit  
-Evaluated KEIL, CooCox, true studio, and OpenOCD scripts/IDEs  
 -Got GPIO code running on hardware  
-Got code reading from ADC running on hardware  
-Provided insight for schematic  
-Evaluated layout and schematic for meeting with Greenberg  
-Began final code  
-Provided links to Google CHROME usb device GUI examples  
-Provided CHROME app GUI template code to team w/ USB HID device access  
  
NEXT WEEK:  
-Meet with Greenberg with team for schematic evaluation  
- Continue development on final code implementing I2C, SDIO, ADC, and associated DMA for our hardware  
-Have board layout sent to OshPark after Greenberg feedback  
-Seek feedback from sponsors on specific VOC compounds  
-Seek feedback from sponsors on desires for physical sensor placement/sensitivity levels  
  
ISSUES:  
-Documentation is lacking  
-Minimal feedback/sponsor funding  
-Hardware/code behind schedule  
-Communication  
-GUI still has no interface, value passing has not been established for ICP  
-Sensor interface has not been outlined  
-wiki has not been established